| L Number | Hits | Search Text | DB | Time stamp | |
|----------------|------|---|--------------------------------|------------|-------|
| 1 | 46 | controller same (activate adj keys) | USPAT; | 2003/06/19 | 11:08 |
| 2 | 1 | 20030032486.pn. and controller | US-PGPUB USPAT; US-PGPUB | 2003/06/19 | 11:47 |
| 3 | 1 | 20030032486.pn. and decoder | USPAT; US-PGPUB | 2003/06/19 | 11:47 |
| _ | 1 | packing adj unpacking adj (ic or | USPAT; US-PGPUB | 2003/01/27 | 11:16 |
| - | 1 | <pre>(integrated adj circuit)) updating adj data adj (ic or (integrated adj circuit))</pre> | USPAT; US-PGPUB | 2003/01/27 | 09:58 |
| - | 2 | (structure or device or apparatus) adj (packing or unpacking) adj (ic or | USPAT; US-PGPUB | 2003/01/27 | 10:06 |
| _ | 1 | <pre>(integrated adj circuit)) (upper adj plate) and (coding adj buttons) and (pcb or (printed adj circuit adj</pre> | USPAT; US-PGPUB | 2003/01/27 | 10:08 |
| - | 2973 | board)) decoder and controller and (pcb or | USPAT; | 2003/06/19 | 11:08 |
| - | 2015 | <pre>(printed adj circuit adj board)) (decoder and controller and (pcb or (printed adj circuit adj board))) and (ic</pre> | US-PGPUB USPAT; US-PGPUB | 2003/01/27 | 10:19 |
| - | 2015 | or (integrated adj circuit)) ((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or | USPAT; US-PGPUB | 2003/01/27 | 10:22 |
| _ | 1 | <pre>(printed adj circuit adj board)) (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre> | USPAT; US-PGPUB | 2003/01/27 | 10:23 |
| - | 25 | (coding adj buttons) (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and | USPAT; US-PGPUB | 2003/01/27 | 10:23 |
| - | 820 | trough (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or | USPAT; US-PGPUB | 2003/01/27 | 11:13 |
| - | 18 | <pre>(printed adj circuit adj board))) and keys (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre> | USPAT; US-PGPUB | 2003/01/27 | 11:14 |
| - | 14 | <pre>(upper adj plate) ((((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre> | USPAT; US-PGPUB | 2003/01/27 | 11:14 |
| _ | 3 | (upper adj plate)) and (lower adj plate) method adj (packing or unpacking) adj (ic | USPAT; | 2003/01/28 | 12:27 |
| - _. | 0 | or (integrated adj circuit)) 5705857.pn. and damascene | US-PGPUB USPAT; | 2003/06/14 | 10:19 |
| _ | 1 | 5705857.pn. and variability | US-PGPUB USPAT; US-PGPUB | 2003/06/19 | 11:46 |
| _ | 1 | 5777433.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:41 |
| - | 1 | 5298768.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:42 |
| _ | 1 | 6355946.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:43 |
| - | 1 | 5959316.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:43 |
| - | 1 | 6407411.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:44 |
| - | 1 | 6069440.pn. | USPAT; US-PGPUB | 2003/06/14 | 10:21 |
| _ | 1 | 6045240.pn. | USPAT; US-PGPUB | 2003/01/29 | 09:44 |

| - | 0 | 5705857.pn. and damascene | USPAT; US-PGPUB | 2003/06/14 10:19 |
|-----|------|--|---|--------------------|
| _ | 1765 | (keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller | USPAT; US-PGPUB; EPO; JPO; | 2003/06/14 10:24 |
| _ | 0 | circuit adj board)) and decoder and | DERWENT; IBM_TDB USPAT; US-PGPUB; | 2003/06/14 10:25 |
| | | controller and (packing adj (ic or (integrated adj circuit))) | EPO; JPO; DERWENT; IBM_TDB | 0000 (05 (14 10 07 |
| | 3 | <pre>(keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller and (unpacking adj (ic or (integrated adj circuit)))</pre> | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:27 |
| _ | 0 | (keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller and (((preserving or updating) adj data) adj (ic or (integrated adj circuit))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:28 |
| - | 2 | <pre>((((preserving or updating) adj data) adj (ic or (integrated adj circuit)))</pre> | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:39 |
| _ | 0 | safe adj data adj updating adj process | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:35 |
| _ | 0 | update adj data adj safely | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:35 |
| - | 0 | prevent adj theft adj data adj (ic or (integrated adj circuit)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:36 |
| _ | 0 | prevent adj copying adj data adj (ic or (integrated adj circuit)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:36 |
| _ | 0 | prevent adj duplication adj data adj (ic or (integrated adj circuit)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:39 |
| - • | 0 | prevent adj duplication adj data adj ic | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:39 |
| _ | 1 | ((preserving or updating) adj data) adj ic | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/06/14 10:41 |
| - | 0 | prevent adj ic adj theft | USPĀT; US-PGPUB; EPO; JPO; DERWENT; | 2003/06/14 10:42 |
| | | | IBM TDB | |

| - | 0 | prevent adj ic adj stolen | USPAT; | 2003/06/14 | 10:42 |
|-----|-----|---|------------------------|------------|--------|
| | | | US-PGPUB; | | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| ļ | | | IBM_TDB | 2003/06/14 | 10.45 |
| _ | 0 | stop adj ic adj stolen | USPAT; US-PGPUB; | 2003/06/14 | 10:45 |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM TDB | | ŀ |
| | 32 | encasing adj (pcb or (printed adj circuit | USPAT; | 2003/06/18 | 16:04 |
| | 32 | adj board)) | US-PGPUB; | 2003/00/10 | 10.0. |
| | | ady boardy) | EPO; JPO; | | |
| İ | | | DERWENT; | | |
| | | | IBM TDB | | |
| _ | 409 | securing adj (pcb or (printed adj circuit | USPAT; | 2003/06/14 | 10:50 |
| ľ | 100 | adj board)) | US-PGPUB; | | 1 |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 1 | (securing adj (pcb or (printed adj circuit | USPAT; | 2003/06/14 | 10:52 |
| | | adj board))) and theft | US-PGPUB; | | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | _ | | IBM_TDB | 0000106111 | 10 50 |
| - | 0 | (securing adj (pcb or (printed adj circuit | USPAT; | 2003/06/14 | 10:52 |
| | | adj board))) and stealing | US-PGPUB; | | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | 2 | (in | IBM_TDB USPAT; | 2003/06/14 | 10.54 |
| - | 3 | (securing adj (pcb or (printed adj circuit adj board))) and copying | US-PGPUB; | 2003/00/14 | 10.54 |
| | | adj board/// and copyrng | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM TDB | | |
| _ | 0 | protect adj program adj stored adj ic | USPAT; | 2003/06/14 | 10:54 |
| | | | US-PGPUB; | | , |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 0 | protect adj program adj ic | USPAT; | 2003/06/14 | 11:08 |
| | | | US-PGPUB; | | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | _ | | IBM_TDB | 2002/05/14 | 11.00 |
| - | 2 | unpacking adj encased adj pcb | USPĀT; | 2003/06/14 | TT: OR |
| | | | US-PGPUB; EPO; JPO; | | |
| 1 | | | DERWENT; | | |
| | | | IBM TDB | | |
| _ | 52 | pc adj card adj housing | USPAT; | 2003/06/18 | 16:11 |
| | | po day cara day nodorny | US-PGPUB; | -000,00,10 | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM TDB | | |
| 1 - | 78 | (pcb or (printed adj circuit adj board)) | USPAT; | 2003/06/18 | 16:12 |
| | | adj protection | US-PGPUB; | | |
| | | | EPO; JPO; | | |
| | | | DERWENT; | 1 | |
| | | | IBM TDB | | |